TAB HALL OF HONOR RECIPIENT NOMINATION FORM

CANDIDATE NAME: AL DUNLOP

BIOGRAPHY: Dr. Dunlop received his BEE from the University of Delaware, MSEE and PhD from Carnegie-Mellon University in 1975, 1977 and 1979 respectively. He holds 14 patents in the areas of circuit design, system initialization and algorithms

For the past 16 years, he has been president of Crossbow Consulting, LLC where he works for clients on topics including circuit design, Electronic Design Automation (EDA), system architecture, large system initialization. He was founder and VP of Engineering at a startup 2002. Dr. Dunlop worked at Bell Laboratories 25 years where he held research and development positions. He was responsible for the IC layout tools in the mid-1980s. He later built clock recovery circuits for packet systems. In research, he held the position of Director of Design Principles Research and later Director of Optical Data Systems Research before retiring in 2001.

CITATION:** **For service as TAB Treasurer, increasing TAB financial transparency, and the use of business plans within TAB.

DESCRIPTION OF ACCOMPLISHMENT(S) RELATIVE TO THE CREATION, DEVELOPMENT, OR ADVANCEMENT OF THE TECHNICAL OBJECTIVES OF THE IEEE:

Dr. Dunlop started IEEE volunteer service as a member of the Circuits and Systems Society (CAS-S) in 1987, beginning in conference activities, adding publications activities, and then multiple Society leadership roles. As a CAS-S leader, he was responsible for the creation of the Council on Electronic Design Automation (CEDA) in 2005. His clarity, diligence and strategic focus made him ideal for founding the Council. Forming a Council by moving elements from a long standing and highly successful Society required a lot of negotiation, brokering compromises, and encouraging collaboration. The most significant accomplishment was moving CAS-S intellectual property and ownership of a publication critical to financial sustainability of the new Council from CAS-S to CEDA. He created a multi-year business plan to administer the transition; the plan was strongly endorsed by both entities. Starting with a solid foundation, EDA has been a financial and technical success. Including CEDA formation, Dr. Dunlop has also done four things far beyond the normal volunteer role that qualify him for the Hall of Honor.

As a member of the TAB Management Committee (TMC), he was a vocal champion for needing a robust business plan as a part of Council formation. A business plan is now a TMC requirement in the Council formation process – because of Dr. Dunlop. As TAB Treasurer, he implemented a Treasurer succession plan. The succession plan created a Deputy Treasurer position providing overlap and training for the next Treasurer, ensuring that management of TAB and S/C finances is handled with a strategic focus and formal, documented continuity. Finally, he was the TAB Treasurer at the start of

the Financial Transparency initiative. Serving on an adhoc committee with TAB leadership and IEEE Financial Planning, Dr. Dunlop had a key role in the creation of new documents, and a vastly improved financial reporting process.

DATE(S) OF ACCOMPLISHMENT(S): These are included within the nomination text and not repeated here

RELEVANT IEEE ACTIVITIES: *In order to stay within the 300 word limit, only the TAB-related activities and IEEE-BoD activities are shown*

IEEE ACTIVITIES:

COMMITTEES/BOARDS:

Technical Activities Board
TAB Periodicals Packages Committee (Chair)
TAB/PSPB Products & Services Committee
Transnational Committee
TAB-Ethics and Conflict Resolution Committee
TAB-Finance Committee (Chair)
TAB-Management Committee(TMC)
TMC-Business Plan ad hoc (Chair)
TMC-Structure ad hoc
IEEE-Members' Dues ad hoc
IEEE-Employee Benefits and Compensation Committee
IEEE Finance Committee
IEEE-Investment Committee
IEEE-Foundation-Awards Committee
IEEE BoD Division 1 Director-Elect

SECTIONS/CHAPTERS:

Schenectady Section	Member	2003-2018

SOCIETY (IES):

Circuits and Systems Society	Associate Editor IEEE Transactions on Computer Aided Design of Circuits and Systems	1987-1990
Circuits and Systems Society	AdCom	1988-1990
Circuits and Systems Society	Editor-in-Chief Transactions on Computer Aided Design of Circuits and Systems	1990-1992
Circuits and Systems Society	Board of Governors	1997-1999
Circuits and Systems Society	Vice President Region 1-6	2000
Circuits and Systems Society	Board of Governors	2003-2005
Council on Electronic Design Automation	President (Founding President)	2005-2007

Council on Electronic Design Automation	Head Nominations and Appointments Committee	2008-2009
Council on Electronic Design Automation	Awards Committee, Kaufman Committee, Nominations and Appointments Committee, Conference Committee	2010-2011

CONFERENCES:

International Conference on Computer Aided Design	Program Committee	1986-1991
Design Automation Conference	Program Committee	1986-1989
Design Automation Conference	Technical Program Chair	1990-1991
Hardware-Software Co-Design Workshop	Co-General Chair	1992
Design Automation Conference	General Chair (over 16000 badges printed)	1993
Design Automation Conference	Executive Committee	1990-1994 2003-2005 2010-2012

OTHERS:

T-CAD	Associate Editor	1987-1990
T-CAD	Editor	1990-1992