

TAB HALL OF HONOR RECIPIENT NOMINATION FORM



CANDIDATE NAME: LEWIS TERMAN

BIOGRAPHY: Lewis Terman received a BS degree in Physics in 1956 and an MS and PhD in Electrical Engineering in 1958 and 1961 from Stanford University. He retired from IBM's Research Division in 2006 after forty-five years, and currently holds the title of IBM Research Emeritus. His past work focused on solid-state circuits, semiconductor technology, memory design and technology, digital and analog circuits and processor design. He received nine major IBM technical awards, including three corporate awards. He was elected to the IBM Academy of Technology in 1991, and served as President of the Academy in 2001-2003. He was elevated to [IEEE Fellow](#) in 1975 for contributions to the design and development of semiconductor computer memory and logic circuitry. He has been active as an IEEE volunteer and member for over fifty years. He served as [IEEE President](#) in 2008.

CITATION:

For exceptional leadership of IEEE Technical Activities and expanding its suite of activities.

DESCRIPTION OF ACCOMPLISHMENT(S) RELATIVE TO THE CREATION, DEVELOPMENT, OR ADVANCEMENT OF THE TECHNICAL OBJECTIVES OF THE IEEE:

Dr Terman started IEEE volunteer service as a member of the Solid State Circuits Council in 1972, spending many years in publications and then conference activities. Supporting first SCCC/SCCS and then ED-S, he was editor and special issue guest editor of four publications, and has been on the program committee and served as program chair and general chair of seven conferences. He is responsible for the creation of six new S/C conferences. As a CA-S member, he had a lead role in the creation of the Council on Electronic Design Automation. As an SSCC member he created the first IEEE Journal special issue annual series from a conference held outside of Regions 1-6. Terman led the activity to transition the Solid State Circuits Council to a Society in 1997. His clarity, diligence and strategic focus made him ideal for governance activities. Over the years he has been involved in governance of 3 Societies (ED-S President, SSCS President, CA-S Board of Governors member). Terman served as TAB Treasurer from 1995-1998. This was a financially unsettling time, as it was during this period that the depletion of the General Fund and its implications became clear. As TAB Treasurer and then volunteer, Terman was instrumental working with the IEEE BoD and TAB to structure a new financial model for the entity. He served as the TAB Vice-President in 2001. As TAB Treasurer he created the TAB support line, which has become the mechanism for funding TAB activities, separating TAB operations from S/C operations. This the first move towards financial transparency in the new financial model. Throughout his career, he has demonstrated a passion for recognizing peers with awards. He was the nominator for 10 successful IEEE Fellow elevations, 2 successful TFA recipients, and 2 successful Medal recipients, including the IEEE Medal of Honor.

DATE(S) OF ACCOMPLISHMENT(S): These are included within the nomination text and not repeated here

RELEVANT IEEE ACTIVITIES:

In order to stay within the 300 word limit, only the TAB-related activities and IEEE-BoD activities are shown

TAB

President, ED-S	1990-1991
TAB Restructuring Committee	1991-1992
TAB AdHoc budget review committee	1992
TAB Technical Meeting Committee Chair	1993-1994
Society Review Committee	1992-1994
Society Review Committee Chair	1995
TAB SPARC Committee	1991 – 1994
TAB SPARC Committee Chair	1995
TAB Treasurer	1995 – 1998
TAB Blue Ribbon Committee on Restructuring	1996 – 1997
President, SSCS	1998- 1999
RAB/TAB Section/Chapter Support Committee	2001
TAB Strategic Planning Committee	2001- 2002
TAB Nominations & Appointment Committee	2003 – 2003, 2005
TABARC	1994-1995
TABARC Chair	2003 – 2004
New Technology Directions Committee	2004 – 2005
TAB VP-Elect	2000
TAB VP	2001
TAB Past VP	2002
Division 1 Director	2004 - 2005

Society/Council

SSCC/SSCS	
- Guest Editor, JSSC	1972, 1973
- Editor, JSSC	1974 – 1977
- Associate Editor, JSSC	1973 – 1974
- European Solid-State Circuits Conf Technical Program Committee	1975 – 1977, 1985
- Executive Committee, VLSI Symposium	1982 – 1997
- Int'l Solid State Circuits Conf	1983
- Int'l Symp on VLSI Technology, Systems And Applications (Taiwan) Tech Pgm Committee	1985, 1989 – 2005
- Int'l Symp on VLSI Technology, Systems And Applications (Taiwan) Tech Pgm Committee Chair	1987
- Co-Chair, Symp on VLSI Technology	1985, 1986
- Co-Chair, Symp on VLSI Circuits	1988, 1989

- Treasurer 1989 - 1990
- Awards committee chair 1996 - 1997
- President 1989 - 1999
- Newsletter Editor 2000 - present
- Chair, Int'l Symp on Low Power Electronics & Design 1994

ED-S

- Transaction, Electron Devices Guest Editor 1979, 1980, 1985
- Awards Committee Chair 1988 - 1990
- President 1990 - 1991

CA-S

- Board of Governors 1981 - 1983
- Long Range Planning Committee 1995

IEEE BoD-Level

- IEEE N&A Committee 1993 - 1994
- History Committee 1995 - 1996
- Membership Development Committee 2001
- IEEE Finance Committee 2003, 1995 - 1998
- WISE Committee 2005
- Infrastructure Oversight Committee 2004 - 2005
- IEEE ExCom 2001

- IEEE President- Elect 2007
- IEEE President 2008
- IEEE Past- President 2008